

Web Images Videos Maps News Shopping Gmail more ▼

Web History | Search settings | Sign in



automatic generation of implemer Search

Instant is off ▼

About 3,400 results (0.29 seconds)

Advanced search

Everything  
More

Search near...

Enter location

Set

Show search tools

## Scholarly articles for **automatic generation of implementations for dsp transforms on fused multiply add architectures**



... for DSP transforms on fused multiply-add architectures - Voronenko -

Cited by 12

SPIRAL: Code generation for DSP transforms - Puschel - Cited by 296

Floating-point fused multiply-add architectures - Quinelli - Cited by 6

## [PDF] View or Download - **AUTOMATIC GENERATION OF IMPLEMENTATIONS FOR DSP ...**

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - Cited by 10 - Related articles

**AUTOMATIC GENERATION OF IMPLEMENTATIONS FOR DSP TRANSFORMS ON FUSED MULTIPLY-ADD ARCHITECTURES.** Yevgen Voronenko and Markus Puschel ...  
citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.58.4525&rep...

## CiteSeerX — **Automatic Generation Of Implementations For DSP ...**

by Y Voronenko - 2004 - Cited by 10 - Related articles

**Automatic Generation Of Implementations For DSP Transforms On Fused Multiply ...**  
citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.58... - Cached

Show more results from psu.edu

## **Automatic Generation of Implementations for DSP Transforms on ...**

by Y Voronenko - Cited by 10 - Related articles

**Automatic Generation of Implementations for DSP Transforms on Fused Multiply-Add Architectures** Preprint (95 KB) · Published paper (link to publisher) ...  
spiral.ece.cmu.edu:8080/pub-spiral/abstract.jsp?id=4 - Cached

## Yevgen Voronenko

**Automatic Generation of Implementations for DSP Transforms on Fused Multiply ...**  
www.ece.cmu.edu/~yvoronen/publications.html - Cached

## Publications of Markus Puschel (Pueschel)

**Automatic Generation of Implementations for DSP Transforms on Fused Multiply ...**  
www.ece.cmu.edu/~pueschel/publications.html - Cached - Similar

Show more results from cmu.edu

## IEEE Xplore - **Automatic generation of implementations for DSP ...**

by Y Voronenko - 2004 - Cited by 10 - Related articles

Aug 26, 2004 ... **Automatic generation of implementations for DSP transforms on fused multiply-add architectures.** 1327057 abstract; Download Citations; Email ...  
ieeexplore.ieee.org/xpls/abs\_all.jsp?arnumber=1327057

## **A Configurable Length, Fused Multiply-Add Floating Point Unit for ...**

by VA Chouliaras

[2] Y. Voronenko and M. Puschel, "**Automatic Generation of Implementations ...**  
ieeexplore.ieee.org/iel5/5379508/5397993/05398088.pdf?...

Show more results from ieee.org

## SPIRAL Publication

**Automatic Generation of Implementations for DSP Transforms on Fused Multiply-Add Architectures** Proc. International Conference on Acoustics, Speech, ...  
www.spiral.net/publications/publ-topic.html - Cached - Similar

## Award#0234293 - **SOFTWARE: ACR: Advanced Code Generation for ...**

**"Automatic Generation of Implementations for DSP Transforms on Fused Multiply-Add Architectures,"** Proc. Intl. Conference on Acoustics, Speech, ...  
www.nsf.gov/awardsearch/showAward.do?AwardNumber=0234293 - Cached

[PPT] Powerpoint - No Slide Title

File Format: Microsoft Powerpoint - View as HTML

**Automatic generation of fast implementations for DSP ... Architecture** conscious – machine dependent; Obsolete when platform is changed/upgraded ... Generator of optimized **DSP transform implementations. DSP transform.** specifies .... Single Instruction Multiple Data (SIMD); **Fused Multiply-Add (FMA)**  
[www.ll.mit.edu/HPEC/agendas/proc03/.../gadic.ppt](http://www.ll.mit.edu/HPEC/agendas/proc03/.../gadic.ppt) - Similar

**1** 2 3 4 5 6 7 8 9 10

**Next**

automatic generation of implemen Search

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

Web Images Videos Maps News Shopping Gmail more ▼

Web History | Search settings | Sign in



code generation for expressions v Search

Instant is off ▼

About 6 results (0.27 seconds)

Advanced search

Everything

More

Search near...

Enter location

Set

Show search tools

Did you mean: code generation for expressions with common subexpressions Top 2 results shown

### Code Generation for Expressions with Common Subexpressions

by AV Aho - 1977 - Cited by 148 - Related articles

This paper shows the problem of generating optimal **code** for **expressions** containing **common subexpressions** is computationally difficult, even for simple ...

portal.acm.org/citation.cfm?id=321992.322001 - Similar

### Code generation for expressions with common subexpressions ...

by AV Aho - 1976 - Cited by 148 - Related articles

If the set of **expressions** has no **common sub-expressions**, then a number of efficient optimal **code generation** algorithms are known for wide classes of ...

portal.acm.org/citation.cfm?id=800168.811537

### Results for: **code generation for expressions with common subexpressions**

#### Data path synthesis from a microcontroller instruction set ...

by HG Haeck - 1991 - Cited by 2 - Related articles

... on the algorithm like loop nrolling or **common subezpression** elimination are ...

Hardware synthesis deals with the **generation** of a microprogrammed proces sor ...

Different parts of the old **expression** trees are now linked to the **common 'G'** .... A re targetable compiler generates the **code** and information for the ...

linkinghub.elsevier.com/retrieve/pii/016560749190345T

#### Process graph analyzer: a front end tool for VHDL behavioral ...

by J Bhasker - 2002 - Cited by 8 - Related articles

synthesis, design analysis and the microcode **generation**. The MIMOLA methodology structures the ..... search for **common** subexpressions. Also, subexpressions ... An **expression** and its syntax tree. ,-. **Code** motion: A process graph may contain loops (a

loop ... Global **subezpression** elimination: In section 4.2.b, we ...

ieeexplore.ieee.org/iel2/196/537/00011772.pdf

#### Pdf - A Combinator-based Compiler for a Functional Language

by P Hudak - 1984 - Cited by 35 - Related articles

global **common**-subexpresssion elimination become trivial, and we are able to eliminate other sources of ..... **Code Generation**. Once the optimized combinator **expression** is con- .... strict in one of its **subezpressions**, namely a share. ...

portal.acm.org/ft\_gateway.cfm?id=800523&type=pdf - Similar

#### Pdf - Detecting Static Algorithms by Partial Evaluation

by S Lisper - 1991 - Cited by 7 - Related articles

to be useful. Lemma. 1 For all  $n > 0$  and for all **expressions**  $e$ , free from ...

portal.acm.org/ft\_gateway.cfm?id=115870&type=pdf

Show more results from acm.org

#### Independent AND-parallel implementation of narrowing

by H Kuchen - 1992 - Cited by 15 - Related articles

hough the notion **subezpression** parallelism might be more appropriate, we will still ... in **common**, this may lead to problems. Consider the following simple rules: .... The **code** for evaluating the **expression**  $e$  has the following properties: ..... D. DeGroot: Restricted And-parallelism, Conf. on 5th **Generation** Comp. ...

www.springerlink.com/index/y38h13v204u14272.pdf

#### [PDF] Elements of Relational Database Theory

File Format: PDF/Adobe Acrobat - Quick View

by PC Kanellakis - 1919 - Cited by 226 - Related articles

tree of its **generation**. Each internal node of this tree is labeled by a ... R is used as a relational variable and all these **expressions** are **subexpressions** of E3, ... the class of project-join **expressions**. Database theory has developed around ..... Containment in the presence of another **common** type of dependencies, ...

users.dimi.uniud.it/~massimo.franceschet/teatro-sql/Kanellakis.pdf - Similar

Did you mean to search for: code generation for expressions with common subexpressions

code generation for expressions with common subexpressions v Search

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

Web Images Videos Maps News Shopping Gmail more ▼

Web History | Search settings | Sign in



dag compiler optimization table pa Search

Instant is off ▼

Page 2 of about 24,300 results (0.36 seconds) Advanced search

Everything

More

Search near...

Enter location

Set

All results

Wonder wheel

More search tools

[PDF] [CSA Introduction to Compiler Design: optimization and backend issues](#)

File Format: PDF/Adobe Acrobat - Quick View

Symbol **table(s)** keep track of scope and binding information about names .... Includes machine specific **optimizations**. Introduction to **Compiler Design** – A. Pimentel – p. 11/98 ..... If the **DAG** is not a tree, then it can be partitioned into multiple trees ... Every target instruction is represented by a tree **pattern** ...

[www.science.uva.nl/~andy/compiler/opt+backend.pdf](http://www.science.uva.nl/~andy/compiler/opt+backend.pdf)  
- Similar

Sponsored links

[Compiler Optimization](#)

Intel brings simplified end-to-end parallelism to Visual Studio C/C++.  
[intel.com](http://intel.com)

See your ad here »

[PDF] [Typing Instructions For SMCC'03 Documents](#)

File Format: PDF/Adobe Acrobat - Quick View

by S Park - 2006 - Related articles

domains such as **compiler optimization** [8] and high-level synthesis [3]. ... We use a conventional **DAG pattern** matching algorithm for our problem [8]. ... priority over 2.3 as shown in **Table 1**. Our **pattern** matching is priority-based ...

[www.jsts.org/html/journal/journal\\_files/2006/.../02\(234-239\).pdf](http://www.jsts.org/html/journal/journal_files/2006/.../02(234-239).pdf)

[PDF] [The Terminology of CS-322](#)

File Format: PDF/Adobe Acrobat - Quick View

by AR Branches

Direct Jump **Table** (as implementation of Switch Stmt). Display Registers ... **Optimization Phase of Compiler**. Order Restrictions (in **DAG-Based Optimization**). Parameters (i.e., Formals) v. ... Tiling – Rules / **Patterns** / Replacement / Cost ...

[web.cecs.pdx.edu/~harry/compiler/CS322-StudyTerms.pdf](http://web.cecs.pdx.edu/~harry/compiler/CS322-StudyTerms.pdf)

[PDF] [2. Intermediate Representation SIMPLE ONLY](#)

File Format: PDF/Adobe Acrobat - Quick View

implicitly includes auxiliary **tables**, e.g., a symbol **table**, a constant, or label **table** ... perform tree walks or use tree **pattern** matching algorithms .... A **DAG** is a contraction of the AST that avoids unnecessary ... Much of the improvement that **compilers** can make in **optimization** comes from context... ...

[www.cs.nmt.edu/~cse423/lectures/3\\_IR.pdf](http://www.cs.nmt.edu/~cse423/lectures/3_IR.pdf)

[Just 3-2 Sem CSE: November 2009](#)

Data flow analysis : Flow graph, data flow equation, global **optimization**, redundant sub expression ... and assignment generic code generation algorithms, **DAG** for register allocation. ... Modern **Compiler** Implementation in C- Andrew N. Appel, Cambridge .... Introduction, Analysis of **pattern** Association Networks, ...

[just3-2.blogspot.com/2009\\_11\\_01\\_archive.html](http://just3-2.blogspot.com/2009_11_01_archive.html) - Cached

[Hive/Design - Hadoop Wiki](#)

Sep 20, 2009 ... The plan generated by the **compiler** (step 5) is a **DAG** of stages with each ... Additionally all the data of a **table** is stored in a directory in hdfs. .... under progress to include other **optimizations** like map-side join. ...

[wiki.apache.org/hadoop/Hive/Design](http://wiki.apache.org/hadoop/Hive/Design) -

Cached - Similar

[\[PDF\] A Fast Fourier Transform Compiler](#)

File Format: PDF/Adobe Acrobat - Quick View

by M Frigo - 1999 - Cited by 269 - Related articles

tains a **table** of all previously simplified **dag** nodes, along with their simplified versions. ... This **pattern** occurs frequently in DFT dags. 6 The scheduler .... Implementing **compiler optimizations** using parallel graph reduction. ...

[supertech.csail.mit.edu/papers/pldi99.pdf](http://supertech.csail.mit.edu/papers/pldi99.pdf)

[\[PDF\] Pathfinder: A Relational Query Optimizer](#)

[Explores XQuery Terrain](#)

File Format: PDF/Adobe Acrobat - Quick View

by J Rittinger - Related articles

2 Relational Query **Optimization** in an XQuery

**Compiler** .... **pattern**—reflects this compositionality.

2.1 Rewriting Large Plan DAGs ... plan **DAGs**. For all

intermediate result **tables**  $t$ , the system infers an approximation  $\alpha$  of ...

[www-db.informatik.uni-tuebingen.de/.../explore-xquery-terrain.pdf](http://www-db.informatik.uni-tuebingen.de/.../explore-xquery-terrain.pdf)

[COMPILER DESIGN HANDBOOK, THE: OPTIMIZATIONS AND MACHINE CODE ... - Google Books Result](#)

Y. N. SRIKANT, PRITI SHANKAR - 2007 - Computers

A simple representation for resource usage is a reservation **table**, ... The resource usage **pattern** is simple when the functional unit is fully pipelined. ...

[books.google.com/books?isbn=142004382X...](http://books.google.com/books?isbn=142004382X...)

[\[PDF\] Simple Code Optimizations](#)

File Format: PDF/Adobe Acrobat - Quick View

by DR HANSON - 1983 - Cited by 14 - Related articles

These **patterns** permit a simple code generator to produce substantially better code. ... pass

**compilation**, simple to implement as a part of **DAG**

construction, ... correctness motivates recent **table-**

driven approaches to code generation. ...

[drhanson.s3.amazonaws.com/storage/documents/simpleopt.pdf](http://drhanson.s3.amazonaws.com/storage/documents/simpleopt.pdf)

[Previous](#)

1 2 3 4 5 6 7 8 9 10 11

[Next](#)

dag compiler optimization table pattern Search

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▼](#)
[Web History](#) | [Search settings](#) | [Sign in](#)

 

Instant is off ▼

About 24,300 results (0.19 seconds)

[Advanced search](#)

Everything

More

Search near...

All results

[Wonder wheel](#)[More search tools](#)

**[PDF] [DAGON: Technology Binding and Local Optimization by DAG Matching ...](#)**

File Format: PDF/Adobe Acrobat - Quick View

by K Keutzer - Cited by 450 - Related articles

Before going into how **compiler** techniques are used in DAGON we will first take a step back to view the ..... sequential circuits is that **optimization patterns** which use the .... In this **table** statistics for DAGON are shown side-by- ...  
[citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.128.1780...](http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.128.1780...)

**[ASCII - USENIX: The Advanced Computing Systems Association](#)**

by LR Nackman - Cited by 10 - Related articles

We highlight the advantages of the base-class composition **pattern** and .... The Figure 2: A class **DAG** extending the **DAG** in Figure 1 to include .... Minor **optimizations** aside, this scheme adds to each object one virtual function **table** pointer per .... For this reason, we investigated **compiler optimization** for this ...  
[www.usenix.org/publications/library/proceedings/c++94/.../nackman.a](http://www.usenix.org/publications/library/proceedings/c++94/.../nackman.a)

**[PDF] [Typing Instructions For SMCC'03 Documents](#)**

File Format: PDF/Adobe Acrobat - Quick View

by S Park - Related articles

domains such as **compiler optimization** [8] and high-level ... We use a conventional **DAG pattern** matching algorithm for our problem [8]. ... priority over 2.3 as shown in **Table 1**. Our **pattern** matching is priority-based peephole ...  
[compiler.snu.ac.kr/shparkid/research/isocc06-sanghyun.pdf](http://compiler.snu.ac.kr/shparkid/research/isocc06-sanghyun.pdf)

**[Cs1352 Compiler Design](#)**

Define a symbol **table**. What are the functions of preprocessors? Define Assembly Code. Define tokens, **Patterns** and lexemes. What are the possible errorrecovery ... What is the purpose of **DAG**? What is the intermediate code representation for ... PART – B 1 (a) Explain the principle sources of **optimization** in detail. ...

[www.scribd.com/doc/24604614/Cs1352-Compiler-Design](http://www.scribd.com/doc/24604614/Cs1352-Compiler-Design) - Cached - Similar

**[PS] [Abstract 1 Introduction](#)**

File Format: Adobe PostScript - View as HTML

by S Cadambi - Related articles

backs and inadequacies of the **compiler optimization**. passes. In order to optimize compilation time and ef- ..... the global hash **table** we are able to identify a **pattern** ... **DAG**. However, the exponential search time limits ...  
[www.ece.cmu.edu/research/piperench/cpr.ps.gz](http://www.ece.cmu.edu/research/piperench/cpr.ps.gz)

**[PDF] [Efficient Generation and Execution of DAG-Structured Query Graphs](#)**

File Format: PDF/Adobe Acrobat - Quick View

by T Neumann - Cited by 9 - Related articles

**DAG**-structured query evaluation plans. Second, it describes the first plan ..... that separated the query **compiler** from the concrete **optimization** steps, such .... first determines the possible access paths (**table** scans or index scans) ...  
[www.mpi-inf.mpg.de/~neumann/diss.pdf](http://www.mpi-inf.mpg.de/~neumann/diss.pdf) - Similar

**[IEEE Xplore - Computer Science and Information Technology, 2009 ...](#)**

Design and implementation of code motion based on **DAG** code list ... Generally, **compiler optimization** divides into machine-independent ... This paper investigated the feasibility to design and fabricate a sacrificial **pattern** for a removable partial ... logic block (CLB) and look up **table** (LUT) as logic elements. ...

[ieeexplore.ieee.org/xpl/locresult.jsp?isnumber=5234374](http://ieeexplore.ieee.org/xpl/locresult.jsp?isnumber=5234374)

**[PS] [A Fast Fourier Transform Compiler Matteo Frigo MIT Laboratory for ...](#)**

File Format: Adobe PostScript - View as HTML

by M Frigo - 1999 - Cited by 268 - Related articles

tains a **table** of all previously simplified **dag** nodes, along, with their simplified versions. ...

This **pattern** occurs frequently in DFT dags. 6 The scheduler ..... Implementing **compiler optimizations**, using parallel graph reduction. ...

[www.fftw.org/pd199.ps.gz](http://www.fftw.org/pd199.ps.gz) - Similar

### The LLVM Target-Independent Code Generator

Nov 8, 2005 ... SelectionDAG **Optimization** Phase: the **DAG** Combiner ... reusable

**patterns** that get inlined into your **patterns** during **compiler-compiler** time. ...

[llvm.org/releases/1.6/docs/CodeGenerator.html](http://llvm.org/releases/1.6/docs/CodeGenerator.html) - Cached - Similar

### LNCS 4128 - Compiler Technology for Blue Gene Systems

by S Kral - 2006 - Cited by 1 - Related articles

**Table 2.** Instruction Count for Data Reordering Operations. .... constants within the **DAG**.

Apart from commonplace **compiler optimizations** [16], ... code **patterns** typically occurring in FFT codes), and (iii) reduce the total number ...

[www.springerlink.com/index/u154w52521314th7.pdf](http://www.springerlink.com/index/u154w52521314th7.pdf)

**1** 2 3 4 5 6 7 8 9 10

**Next**

dag compiler optimization table pattern Search

Search within results

Search Help

Give us feedback

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)



Web Images Videos Maps News Shopping Gmail more ▼

Web History | Search settings | Sign in



efficient tree pattern matching an

Search

Instant is off ▼

About 36,700 results (0.27 seconds)

Advanced search

Everything

Books

More

Search near...

Enter location

Set

Show search tools

### Code generation using tree matching and dynamic programming

by AV Aho - 1989 - Cited by 331 - Related articles

Alfred V. Aho , Mahadevan Ganapathi, **Efficient tree pattern matching** (extended abstract): an aid to code generation, Proceedings of the 12th ACM ...  
portal.acm.org/citation.cfm?id=75700 - Similar

### Pdf - Efficient Tree Pattern Matching: an Aid to Code Generation ...

by AV Aho - 1985 - Cited by 70 - Related articles

**Efficient Tree Pattern Matching: an Aid to Code Generation.** (Extended ...  
portal.acm.org/ft\_gateway.cfm?id=318663&type=pdf

### Efficient tree pattern matching (extended abstract)

by AV Aho - 1985 - Cited by 70 - Related articles

**Efficient tree pattern matching** (extended abstract): an aid to code ...  
portal.acm.org/citation.cfm?id=318593.318663

Show more results from acm.org

### CiteSeerX — Efficient retargetable code generation using bottom-up ...

by A Balachandran - 1990 - Cited by 50 - Related articles

94, Code generation using tree matching and dynamic programming – Aho, Ganapathi, ...  
22, **Efficient Tree Pattern Matching: An Aid to Code Generation** – Aho, ...  
citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.156.3846 - Cached

### CiteSeerX — A New Algorithm for Linear Regular Tree Pattern Matching

by P Shankar - 1998 - Cited by 11 - Related articles

27, Tree Automata for Code Selection – Ferdinand, Seidl, et al. 22 ...  
citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.122.7851 - Cached

Show more results from psu.edu

### Efficient retargetable code generation using bottom-up tree ...

by A Balachandran - 1990 - Cited by 50 - Related articles

Aho, A. V. and Ganapathi, M. **Efficient tree pattern matching: An aid to code generation.**  
Proc. 12th ACM Symposium on Principles of Programming Languages, ...  
linkinghub.elsevier.com/retrieve/pii/009605519090006B

### A new algorithm for linear regular tree pattern matching - Elsevier

by P Shankar - 2000 - Cited by 11 - Related articles

A.V. Aho, M. Ganapathi, **Efficient tree pattern matching: an aid to code ...**  
linkinghub.elsevier.com/retrieve/pii/S0304397598002059

Show more results from elsevier.com

### Register allocation in a pattern matching code generator

by WR Van Bijl - 1987 - Related articles

A. V. Aho and M. Ganapathi, '**Efficient tree pattern matching: an aid to code generation**',  
Proceedings. 3. A. Scheuneman, 'A machine independent approach to ...  
www3.interscience.wiley.com/journal/113446869/articletext?...

### COMPILER DESIGN HANDBOOK, THE: OPTIMIZATIONS AND MACHINE CODE ... - Google Books Result

Y. N. SRIKANT, PRITI SHANKAR - 2007 - Computers

Communications of the ACM 18(6):333–40. 2. AV Aho and M. Ganapathi. 1985. **Efficient tree pattern matching: An aid to code generation.** ...  
books.google.com/books?isbn=142004382X...

### Two tree pattern matchers for code selection

by B Weisgerber - 1989 - Cited by 11 - Related articles

A.V. Aho, M. Ganapathi, **Efficient Tree Pattern Matching: An Aid to Code. Generation,**

POPL 1985. M. Ganapathi, C.N. Fischer, Description-Driven **Code ...**  
[www.springerlink.com/index/752p93v68650h015.pdf](http://www.springerlink.com/index/752p93v68650h015.pdf)

**1** 2 3 4 5 6 7 8 9 10

**Next**

efficient tree pattern matching an

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more](#) ▼

[Web History](#) | [Search settings](#) | [Sign in](#)


fma dag compiler

Search

Instant is off ▼

About 14,100 results (0.47 seconds)

[Advanced search](#)

Everything

More

Search near...

Enter location

Set

[Show search tools](#)

[PDF] [View or Download - AUTOMATIC GENERATION OF IMPLEMENTATIONS FOR DSP ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - Cited by 10 - Related articles

general-purpose **compilers** can only make very restricted use of .... **FMA DAG** D that requires fewer arithmetic operations and, bar- ...

[citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.58.4525&rep...](http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.58.4525&rep...)

[PDF] [FFTs on BG/L Status and Methods](#)

File Format: PDF/Adobe Acrobat - Quick View

F. Franchetti, M. Püschel: „A SIMD Vectorizing **Compiler** for Digital Signal Processing ...

Input: directed acyclic graph (**DAG**) of numerical SSA code ... Vienna MAP Vectorizer with **FMA**, XLC with Intrinsics and C99 ...

[https://asc.llnl.gov/computing\\_resources/bluegenel/.../franchetti.pdf](https://asc.llnl.gov/computing_resources/bluegenel/.../franchetti.pdf)

[Optimal Floating-Point Expression Translation Method Based on ...](#)

by KS Serebryany - 2006

Sep 25, 2008 ... The method of claim 1, wherein the **FMA DAG** comprises a sequence of **FMA** .... What is also needed is a method for an optimizing **compiler** for ...

[www.freepatentsonline.com/y2008/0235676.html](http://www.freepatentsonline.com/y2008/0235676.html) - Cached

[PDF] [SEVERAL modern processor architectures such as Intel](#)

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - 2007 - Related articles

linear transform can be transformed into an **FMA DAG** with all ..... puter with 4 GB of RAM, using the Intel **Compiler** 9.0 with ...

[spiral.ece.cmu.edu:8080/pub-spiral/pubfile/fma\\_51.pdf](http://spiral.ece.cmu.edu:8080/pub-spiral/pubfile/fma_51.pdf)

[PDF] [Register Allocation in Kernel Generators](#)

File Format: PDF/Adobe Acrobat - Quick View

Given **dag**, find schedule of the **dag** and register assignment that minimizes the number of register spills: NP-hard ... We assume (i.e., hope) that the C **compiler** implements the .... 2 **fma**/cycle. ► 2 L1 loads/cycle, in parallel with FPU. ...

[cscads.rice.edu/workshops/july2007/autotune-slides-07/Frigo.pdf](http://cscads.rice.edu/workshops/july2007/autotune-slides-07/Frigo.pdf) - Similar

[Comp.compilers: DDG/DAG construction algorithms](#)

Oct 20, 1990 ... From comp.compilers newsgroup: DDG/DAG construction algorithms.

[compilers.iecc.com/comparch/article/90-10-086](http://compilers.iecc.com/comparch/article/90-10-086) - Cached - Similar

[LNCS 4128 - Compiler Technology for Blue Gene Systems](#)

by S Kral - 2006 - Cited by 1 - Related articles

hole optimizer for utilizing fused multiply-add (**FMA**) instructions, .... constants within the **DAG**. Apart from commonplace **compiler** optimizations [16], ...

[www.springerlink.com/index/u154w52621314th7.pdf](http://www.springerlink.com/index/u154w52621314th7.pdf)

[Earley Parser - Application Forum at ObjectMix.com](#)

Earley Parser **Compilers**. ... returns ALL parse trees (or a **DAG**) - return the parse trees in terms of the original grammar (not a .... [www.ucse.edu.ar/fma/sepa](http://www.ucse.edu.ar/fma/sepa). Kakuy! it's an algorithm animation tool designed to teach Earley's and YCK ...

[objectmix.com/compilers/36945-earley-parser.html](http://objectmix.com/compilers/36945-earley-parser.html) - Cached

[The LLVM Target-Independent Code Generator](#)

Nov 8, 2005 ... This step turns the LLVM code into a **DAG** of target instructions. ....

International Conference on **Compiler** Construction (CC) 2004 ... If a target supports floating point multiply-and-add (**FMA**) operations, ...

[llvm.org/releases/1.6/docs/CodeGenerator.html](http://llvm.org/releases/1.6/docs/CodeGenerator.html) - Cached - Similar

[\[PDF\] Automatically Optimized FFT Codes for the BlueGene/L Supercomputer](#)

File Format: PDF/Adobe Acrobat - Quick View

numerical scalar **DAG** as vectorization input, where the inherent parallelism is .... Spiral  
using the XL C **compiler's** vectorizer and **FMA** extraction turned on ...

[www.isecslab.org/people/pw/papers/vecpar04](http://www.isecslab.org/people/pw/papers/vecpar04)

**1** 2 3 4 5 6 7 8 9 10

**Next**

fma dag compiler

Search

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▼](#)
[Web History](#) | [Search settings](#) | [Sign in](#)


fma dag compiler optimization

Search

Instant is off ▼

Page 4 of about 832 results (0.39 seconds)

[Advanced search](#)**Everything**[Books](#)[More](#)**Search near...**

Enter location

[Set](#)**All results**[Wonder wheel](#)[More search tools](#)[Applications in Class 717/154](#)

Optimizing program code in a static **compiler** by determining the live ranges of .... Each pattern in the table includes an **FMA** (fused multiply-add) **DAG**. ...

[www.freepatentsonline.com/ACC-717-154.html](http://www.freepatentsonline.com/ACC-717-154.html) - Cached

[Scheduling technique for software pipelining - Patent 7331045](#)

by AR Martin - 2008 - Related articles

"A **DAG**-Based Partitioning-Reconfiguring Scheduling Algorithm in Network of ...

[www.freepatentsonline.com/7331045.html](http://www.freepatentsonline.com/7331045.html) - Cached

[Show more results from freepatentsonline.com](#)

[Efficient Utilization of SIMD Extensions](#)

by F Franchetti - 2005 - Cited by 34 - Related articles

Oct 15, 2004 ... eral-purpose **compiler optimization**. Automatic performance ..... SIMD-**FMA** instructions. The third group of rules rewrites unsupported SIMD in- ... of the vector **DAG**. The scheduling algorithm minimizes the ...

[ieeexplore.ieee.org/iel5/5/30187/01386659.pdf?arnumber=1386659](http://ieeexplore.ieee.org/iel5/5/30187/01386659.pdf?arnumber=1386659)

[\[PDF\] Workshop on Formal Methods for Aerospace \(FMA\) M.L. Bujorianu and ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by ML Bujorianu - 2009 - Related articles

**compiler** itself) is formally proved correct, meaning that the generated ..... Then it searches in O for a suitable **optimization** tool. The results ...

[eprints.ma.man.ac.uk/1482/01/covered/MIMS\\_ep2010\\_51.pdf](http://eprints.ma.man.ac.uk/1482/01/covered/MIMS_ep2010_51.pdf)

[\[PDF\] Auto-tuning Performance on Multicore Computers](#)

File Format: PDF/Adobe Acrobat

the auto-tuning approach to program **optimization** and performance portability to the .....

2.1 Comparison of traditional **compiler** and auto-tuning capabilities . . . . . 17 .... **DAG**.

Directed Acyclic Graph. DDR. Double Data Rate (DRAM) ... **FMA**. Fused Multiply-Add.

FMM. Fast Multipole Method ...

[www.eecs.berkeley.edu/Pubs/TechRpts/2008/EECS-2008-164.pdf](http://www.eecs.berkeley.edu/Pubs/TechRpts/2008/EECS-2008-164.pdf)

[Field Modifiable Architecture with FPGAs and its Design ...](#)

by M Fujita - 2003 - Cited by 3 - Related articles

Stage 2: Source Level **Optimization** and Verification. .... matching rules for the **compiler** and ii) the net-list for the functional units. 2.5 Code Generation ... Besides, it can deal with **DAG**. (Direct Acyclic Graph) for input DFGs and pattern graphs. .... tracting communication overhead and our **FMA** design re- ...

[doi.ieeecomputersociety.org/10.1109/HICSS.2003.1174810](http://doi.ieeecomputersociety.org/10.1109/HICSS.2003.1174810)

[The LLVM Target-Independent Code Generator](#)

Aug 31, 2010 ... SelectionDAG **Optimization** Phase: the **DAG** Combiner ... If a target supports floating point multiply-and-add (**FMA**) operations, .... The locals area is where the llvm **compiler** reserves space for local variables. ...

[llvm.org/releases/2.8/docs/CodeGenerator.html](http://llvm.org/releases/2.8/docs/CodeGenerator.html) - Cached

[\[llvm-commits\] CVS: llvm/docs/CodeGenerator.html](#)

Aug 27, 2006 ... +"illegal" **DAG**. A legal **DAG** for a target is one that only uses .... a target supports floating point multiply-and-add (**FMA**) operations, ...

[www.mail-archive.com/llvm-commits@cs.../msg08230.html](http://www.mail-archive.com/llvm-commits@cs.../msg08230.html) - Cached

[ITBHU.org: CSE: B.Tech. Syllabus](#)

Analysis- Synthesis model of compilation, various phases of a **compiler** . ... register allocation, code generation, **DAG** representation of programs, code ... optimizing **compilers** for parallelism, dependency analysis, code **optimization** and scheduling, .... Sony Ericsson

**Fma** Отправка Sms Бесплатная Nokia N96 Perm ...

[www.itbhu.org/chapters/dept/cse/archives/000115.html](http://www.itbhu.org/chapters/dept/cse/archives/000115.html) - Cached - Similar

[PDF] [Empirical evaluation of reasoning in lightweight DLs on life ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by B Sunilsivaraporn - Cited by 1 - Related articles

Through **compilation** of specific require- ments for the medical domain, the 'GALEN Representation ..... graph (**DAG**) where vertices are concept names (strictly speak- .... and extended to other species.8 **FMA** has four interrelated .... interest in the DL community to research on **optimization** ...

[lat.inf.tu-dresden.de/research/papers/2008/Sun-MIWA1-08.pdf](http://lat.inf.tu-dresden.de/research/papers/2008/Sun-MIWA1-08.pdf)

[Previous](#)

1 2 3 **4** 5 6 7 8 9 10 11 12 13

[Next](#)

fma dag compiler optimization

Search

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▼](#)
[Web History](#) | [Search settings](#) | [Sign in](#)


fma dag compiler optimization tab Search

Instant is off ▼

About 10,200 results (0.30 seconds)

[Advanced search](#)

Everything

[More](#)

Search near...

Enter location

[Show search tools](#)

[PDF] [SEVERAL modern processor architectures such as Intel](#)

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - 2007 - Related articles

linear transform can be transformed into an **FMA DAG** with all unfused multiplications collected at the outputs. .... Second, **Table VI** gives pseudocode for the **FMA** optimized ....

**FMA optimization** performed by the **compiler**. For example, ...

[spirat.ece.cmu.edu:8080/pub-spirat/pubfile/fma\\_51.pdf](http://spirat.ece.cmu.edu:8080/pub-spirat/pubfile/fma_51.pdf)

[LNCS 4128 - Compiler Technology for Blue Gene Systems](#)

by S Kral - 2006 - Cited by 1 - Related articles

hole optimizer for utilizing fused multiply-add (**FMA**) instructions, and Section 5 ... **Table 2**.

Instruction Count for Data Reordering Operations. .... constants within the **DAG**. Apart from commonplace **compiler optimizations** [16], ...

[www.springerlink.com/index/u154w52521314th7.pdf](http://www.springerlink.com/index/u154w52521314th7.pdf)

[LNCS 2985 - FFT Compiler Techniques](#)

by S Kral - 2004 - Cited by 13 - Related articles

allows the vectorization of large Fftw codelets (see **Table 2**). ...

[www.springerlink.com/index/bictdf5nvybmi7fl.pdf](http://www.springerlink.com/index/bictdf5nvybmi7fl.pdf)

[Show more results from springerlink.com](#)

[PDF] [FFT Compiler Techniques](#)

File Format: PDF/Adobe Acrobat - Quick View

by S Kral - Cited by 13 - Related articles

using a set of rules to optimize the SIMD **DAG** obtained from the vectorizer. .... **Table 2**.

Efficient Computation of  $ecx := 17 * stride$ . .... **FMA** extraction (but without backend

**optimizations**) and (ii) Fftw codelets vector- ...

[citeseerx.ist.psu.edu/viewdoc/download?doi=10.1...](http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1...) - Similar

[Optimal Floating-Point Expression Translation Method Based on ...](#)

by KS Serebryany - 2006

Sep 25, 2008 ... The method of claim 1, wherein the **FMA DAG** comprises a sequence of

**FMA** .... coupled to an **optimizer** and a **table** of patterns coupled to the optimizer, ... What is also needed is a method for an optimizing **compiler** for ...

[www.freepatentsonline.com/y2008/0235676.html](http://www.freepatentsonline.com/y2008/0235676.html) - Cached

[The LLVM Target-Independent Code Generator](#)

Sep 20, 2010 ... SelectionDAG **Optimization** Phase: the **DAG** Combiner ...

If a target supports floating point multiply-and-add (**FMA**) .... This lowering process is often target

specific, and is responsible for turning jump **table** entries, constant pool .... The locals area is where the llvm **compiler** reserves space for ...

[llvm.org/docs/CodeGenerator.html](http://llvm.org/docs/CodeGenerator.html) - Cached - Similar

[PDF] [Automatically Optimized FFT Codes for the BlueGene/L Supercomputer](#)

File Format: PDF/Adobe Acrobat - Quick View

is used to implement peephole **optimization** on the vector **DAG**. ... **Table 1**. Two-way Vectorization. Two scalar add instructions are transformed into ...

Spiral using the XL C **compiler's** vectorizer and **FMA** extraction turned on, ...

[www.lseclab.org/people/pw/papers/vecpar04](http://www.lseclab.org/people/pw/papers/vecpar04)

[COMPILERS PRINCIPLES TECHNIQUES TOOLS ALFRED AHO MONICA](#)

[LAM RAVI ...](#)

2.7 Incorporating a symbol **table**..... 60 ... 3.9 **Optimization** of DFA-based pattern matchers ..... 134. Exercises . .... 9.8 The **dag** representation of basic

blocks ..... 546 .... **Compilers** Principles Techniques Tools by Alfred Aho Size: 21 MB ...

[ebooksfreepdfdownload.blogspot.com/.../compilers-principles-techniques-tools.html](http://ebooksfreepdfdownload.blogspot.com/.../compilers-principles-techniques-tools.html) -

Cached

[ibmr-49-02-19 437..446](#)

by F Franchetti - 2005 - Related articles

Mar 31, 2005 ... processor features, such as short-vector SIMD or **FMA**. ISA extensions, is imperative. Unfortunately, the methods used ... vectorizer is to completely cover the given scalar **DAG** by .... **Table 1** Relative costs of SIMD operations. .... facility of the XL C **compiler**. Figure 6. General **optimization** rule. ...

ieeexplore.ieee.org/iel5/5288520/5388783/05388799.pdf?amumber...

[Pdf - A New Speculation Technique to Optimize Floating-Point ...](#)

by M Takeuchi - 2003 - Related articles

**compilers, optimization**; G.4 [Mathematical Software]: Efficiency. General Terms .... JITed code for all of the programs in **Table 1** was 4625 bytes, which .... Our FP speculation algorithm runs on a direct acyclic graph (**DAG**) .... example, the IBM XL Fortran [10] **compiler** uses **FMA** by default, ...

portal.acm.org/ft\_gateway.cfm?id=782857&type=pdf

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#)[Next](#) [Search within results](#)[Search Help](#)[Give us feedback](#)[Google Home](#)[Advertising Programs](#)[Business Solutions](#)[Privacy](#)[About Google](#)



[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more](#) ▼

[Web History](#) | [Search settings](#) | [Sign in](#)


fma dag compiler optimization tab Search

Instant is off ▼

About 3,520 results (0.38 seconds)

[Advanced search](#)

Everything

More

Search near...

Enter location

Set

[Show search tools](#)

[PDF] [SEVERAL modern processor architectures such as Intel](#)

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - 2007 - Related articles

Example: The **FMA DAG** in Fig. 1(c) is the output of. Algorithm 1, when applied to the standard **DAG** in ..... Second, **Table VI** gives pseudocode for the **FMA** optimized ... for each **pattern** of trivial entry locations in the propagated diagonals. Therefore, ..... **FMA optimization** performed by the **compiler**. For example, ...  
[spiral.ece.cmu.edu:8080/pub-spiral/pubfile/fma\\_51.pdf](http://spiral.ece.cmu.edu:8080/pub-spiral/pubfile/fma_51.pdf)

[The LLVM Target-Independent Code Generator](#)

Nov 8, 2005... if **tables** and other things are all emitted by tblgen, ..... SelectionDAG

**Optimization Phase: the DAG Combiner** ... If a target supports floating point multiply-and-add (**FMA**) ... At **compiler-compiler** time, it analyzes your instruction **patterns** and tells you if your **patterns** make sense or not. ...

[llvm.org/releases/1.6/docs/CodeGenerator.html](http://llvm.org/releases/1.6/docs/CodeGenerator.html) - Cached - Similar

[The LLVM Target-Independent Code Generator](#)

SelectionDAG **Optimization Phase: the DAG Combiner** ... If a target supports ...
[llvm.org/docs/CodeGenerator.html](http://llvm.org/docs/CodeGenerator.html) - Cached - Similar
[Show more results from llvm.org](#)

[LNCS 4128 - Compiler Technology for Blue Gene Systems](#)

by S Kral - 2006 - Cited by 1 - Related articles

hole optimizer for utilizing fused multiply-add (**FMA**) instructions, and Section 5 ... **Table 2**. Instruction Count for Data Reordering Operations. .... constants within the **DAG**. Apart from commonplace **compiler optimizations** [16], ... code **patterns** typically occurring in FFT codes), and (iii) reduce the total number ...

[www.springerlink.com/index/u154w52521314th7.pdf](http://www.springerlink.com/index/u154w52521314th7.pdf)

[LNCS 2985 - FFT Compiler Techniques](#)

by S Kral - 2004 - Cited by 13 - Related articles

allows the vectorization of large Fftw codelets (see **Table 2**). ...
[www.springerlink.com/index/blctdf5nvybmi7ff.pdf](http://www.springerlink.com/index/blctdf5nvybmi7ff.pdf)
[Show more results from springerlink.com](#)

[Optimal Floating-Point Expression Translation Method Based on ...](#)

by KS Serebryany - 2006

Sep 25, 2008 ... Each **pattern** in the **table** includes an **FMA** (fused multiply-add) **DAG**(Directed. ... The method of claim 1, wherein the **FMA DAG** comprises a sequence of

**FMA** .... coupled to an **optimizer** and a **table** of **patterns** coupled to the optimizer, ... What is also needed is a method for an optimizing **compiler** for ...

[www.freepatentsonline.com/y2008/0235676.html](http://www.freepatentsonline.com/y2008/0235676.html) - Cached

[COMPILERS PRINCIPLES TECHNIQUES TOOLS ALFRED AHO MONICA](#)

[LAM RAVI ...](#)2.7 Incorporating a symbol **table**..... 60 ... 3.9 **Optimization** of DFA-based **pattern** matchers ..... 134. Exercises . .... 9.8 The **dag** representation of basicblocks ..... 546 .... **Compilers** Principles Techniques Tools by Alfred Aho Size:

21 MB ...

[ebooksfreepdfdownload.blogspot.com/.../compilers-principles-techniques-tools.html](http://ebooksfreepdfdownload.blogspot.com/.../compilers-principles-techniques-tools.html) -

Cached

[PDF] [Efficient Utilization of SIMD Extensions](#)

File Format: PDF/Adobe Acrobat - Quick View

by F Franchetti - Cited by 34 - Related articles

**compiler optimization**. Automatic performance tuning systems ... **Table I** gives an overview over the SIMD ISA extensions .... data access **patterns** can be handled efficiently across all ..... of the vector **DAG**. The scheduling algorithm minimizes the .... (**FMA**) instructions.

Note that these speed-up figures do not ...

[citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.130.8746...](http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.130.8746...)

[Pdf - A New Speculation Technique to Optimize Floating-Point ...](#)

by M Takeuchi - 2003 - Related articles

**compilers, optimization**; G.4 [Mathematical Software]: Efficiency. General Terms ..... Our FP speculation algorithm runs on a direct acyclic graph (**DAG**) ... new target ← **pattern** match target longer than target from l. 5. if new target = ∅ break. .... example, the IBM XL Fortran [10] **compiler** uses **FMA** by default, ...  
[portal.acm.org/ft\\_gateway.cfm?id=782857&type=pdf](http://portal.acm.org/ft_gateway.cfm?id=782857&type=pdf)

[Welcome to IEEE Xplore](#)

by F FRANCHETTI - 2005 - Cited by 34 - Related articles

This paper describes special-purpose **compiler** technology that supports automatic ..... iterations, and the resulting data access **patterns** can be handled ..... The MAP vectorizer additionally supports the extraction of SIMD **FMA** instructions. .... system is used to implement peephole **optimization** on the vector **DAG**. ...  
[ieeexplore.ieee.org/iel5/5/30187/1386659/1386659.plain.html](http://ieeexplore.ieee.org/iel5/5/30187/1386659/1386659.plain.html)

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#)

[Next](#)

fma dag compiler optimization tab	Search
-----------------------------------	--------

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more](#) ▼

[Web History](#) | [Search settings](#) | [Sign in](#)


fused multiply add dag

Search

Instant is off ▼

About 5,270 results (0.45 seconds)

[Advanced search](#)

Everything

[More](#)

Search near...

Enter location

Set

[Show search tools](#)

[\[PDF\] View or Download - AUTOMATIC GENERATION OF IMPLEMENTATIONS FOR DSP ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - Cited by 10 - Related articles

Intel Itanium and Itanium 2, feature **fused multiply-add** (FMA) in- .... acyclic graph or **DAG** describing the data flow of the computation. ...[citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.58.4525&rep...](#)

[\[PDF\] Mechanical Derivation of Fused Multiply - SEVERAL modern processor ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - 2007 - Related articles

Input: a **DAG** with **add** and **multiply** nodes only; output: an equivalent **DAG** ..... tions for DSP transforms on **fused multiply-add** architectures," in Proc. ...[spiral.ece.cmu.edu:8080/pub-spiral/pubfile/jfma\\_51.pdf](#)

[IEEE Xplore - Mechanical Derivation of Fused Multiply&#x2013;Add ...](#)

by Y Voronenko - 2007 - Related articles

Aug 16, 2007 ... Several computer architectures offer **fused multiply-add** (FMA), ... We implemented the **DAG**-based version of the method and show that we can ...[ieeexplore.ieee.org/xpls/abs\\_all.jsp?arnumber=4291875](#)

[Multiply-accumulate - Wikipedia, the free encyclopedia](#)

When performed with a single rounding, it is called a **fused multiply-add** (FMA) or **fused ...**A **fused multiply-add** is a floating-point **multiply-add** operation ...[en.wikipedia.org/wiki/Multiply-accumulate](#) - Cached - Similar

[The fused multiply-add instruction leads to algorithms for ...](#)

by FG Gustavson - 1999 - Cited by 2 - Related articles

Many machines provide a double-precision **fused multiply-add** (FMA) instruction that computes  $d = a * b + c$  with  $d$  correctly rounded. ...[portal.acm.org/citation.cfm?id=781999](#) - Similar

[Fused Multiply-add Functional Unit invention](#)

A functional unit is added to a graphics processor to provide direct support for double-precision arithmetic, in addition to the single-precision functional ...

[www.freshpatents.com/-dt20090611plan20090150654.php](#) - Cached - Similar

[\[PDF\] FFTs on BG/L Status and Methods](#)

File Format: PDF/Adobe Acrobat - Quick View

**Fused multiply-add**. • DFT is not locally balanced w.r.t. **adds** and **mults** .... Input: directed acyclic graph (**DAG**) of numerical SSA code ...[https://asc.llnl.gov/computing\\_resources/bluegene/.../franchetti.pdf](#)

[\(WO/2007/114722\) AN OPTIMAL FLOATING-POINT EXPRESSION TRANSLATION ...](#)

by KS SEREBRYANY - 2007

Oct 11, 2007 ... Each pattern in the table includes an FMA (**fused multiply-add**) **DAG** (Directed Acyclic Graph), a canonical form equivalent of the FMA **DAG**, ...[www.wipo.int/patdb/en/wo.jsp?WO=2007114722](#) - Cached

[\[PDF\] Automatically Optimized FFT Codes for the BlueGene/L Supercomputer](#)

File Format: PDF/Adobe Acrobat - Quick View

ports the extraction of SIMD **fused multiply-add** instructions. ... numerical scalar **DAG** as vectorization input, where the inherent parallelism is obvious. ...[www.iscclab.org/people/pw/papers/vecpar04](#)

[PDF] [ensi-00269219\_v1] Integer and Floating-Point Constant ...

File Format: PDF/Adobe Acrobat - Quick View

by N Brisebarre - 2008 - Cited by 7 - Related articles

gorithm which consists of two dependent **fused-multiply-** and **add** operations. .... the **DAG** to minimise the latency. On the other size, it only ... **add** the overhead of the registers, essentially for the lower ...

hal.inria.fr/docs/00/26/92/19/PDF/constmult.pdf

1 2 3 4 5 6 7 8 9 10

[Next](#)

fused multiply add dag

Search

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▼](#)
[Web History](#) | [Search settings](#) | [Sign in](#)


fused multiply add dag compile

Search

Instant is off ▼

About 2,220 results (0.37 seconds)

[Advanced search](#)

Everything

[More](#)

Search near...

Enter location

[Set](#)[Show search tools](#)Did you mean: [fused multiply add dag \*\*compiler\*\*](#)[\[PDF\] Automatically Tuned FFTs for BlueGene/L's Double FPU](#)

File Format: PDF/Adobe Acrobat - Quick View

by F Franchetti - Related articles

This vectorization process translates nodes of the scalar **DAG** into ... cial **fused multiply add** instructions (FMA) provided by BlueGene/L's double. FPU. ... [3] —, “**Compiling** for SIMD within a register,” in Languages and Compilers ...  
[citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.117...](#)

[\[PDF\] Automatically Optimized FFT Codes for the BlueGene/L Supercomputer](#)

File Format: PDF/Adobe Acrobat - Quick View

ports the extraction of SIMD **fused multiply-add** instructions. ... numerical scalar **DAG** as vectorization input, where the inherent parallelism is obvious. .... [11] —, “**Compiling** for SIMD within a register,” in Languages and Com- ...  
[www.isecslab.org/people/pw/papers/vecpar04](#)

[Optimal Floating-Point Expression Translation Method Based on ...](#)

by KS Serebryany - 2006

Sep 25, 2008 ... Each pattern in the table includes an FMA (**fused multiply-add**) **DAG** .... of the **DAG** as **compiled** code for computing the incoming expression. ...  
[www.freepatentsonline.com/y2008/0235676.html](#) - Cached

[LNCS 4128 - Compiler Technology for Blue Gene Systems](#)

by S Kral - 2006 - Cited by 1 - Related articles

This paper describes a version of the MAP **compiler** targeting IBM's Blue ... hole optimizer for utilizing **fused multiply-add** (FMA) instructions, and Section 5 .... maximally cover a scalar **DAG** with SIMD instructions natively supported by ...  
[www.springerlink.com/index/u154w525213141h7.pdf](#)

[\(WO/2007/114722\) AN OPTIMAL FLOATING-POINT EXPRESSION TRANSLATION ...](#)

by KS SEREBRYANY - 2007

Oct 11, 2007 ... Each pattern in the table includes an FMA (**fused multiply-add**) **DAG** (Directed Acyclic Graph), a canonical form equivalent of the FMA **DAG**, and a shape ... and FNMA (**fused negate multiply-add**) instructions as **compiled** ...  
[www.wipo.int/pctdb/en/wipo.jsp?WO=2007114722](#) - Cached

[\[PDF\] Vectorization Techniques for BlueGene/L's Double FPU](#)

File Format: PDF/Adobe Acrobat - Quick View

by F Franchetti - Related articles

First, a dependency analysis is performed on the scalar **DAG**. ..... to fully fold any SIMD data reorganization into SIMD **fused multiply add** instructions. ... [11] —, „**Compiling** for SIMD within a register,” in Languages and Compilers for ...  
[202.114.89.42/resource/pdf/1798.pdf](#)

[\[PDF\] pdf - Markowitz-Type Heuristics for Computing J acobian Matrices ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by A Albrecht - Cited by 16 - Related articles

can be attached to the edges in the **dag**. The notation  $k \prec j$  is used to ... number of scalar **fused multiply-add** floating-point operations (fmas) are per- ..... At the statement and basic block level, c-graphs can be generated at **compile** ...  
[www.mcs.anl.gov/uploads/ceis/papers/P933.pdf](#)

[\[PDF\] High Performance Numerical Components](#)

File Format: PDF/Adobe Acrobat

Extensive application independent **compile**-time analysis. • Integrated performance

modeling and ... Generation of a **DAG**. • Scheduling of instructions. • Unparsing. •  
Implementation .... Multiple FPU's with **fused multiply-add** instructions ...  
[www2.cs.uh.edu/~johnsson/lacsl\\_reviewtalk\\_S.pdf](http://www2.cs.uh.edu/~johnsson/lacsl_reviewtalk_S.pdf)

[\[PDF\] Automatically Customising VLIW Architectures with Coarse Grained ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by D Jain - Cited by 14 - Related articles

Trimaran framework [17] for **compiling** and simulating the resulting IS-extended. VLIW  
machine. .... Acyclic Graph (**DAG**) representation of the enlarged basic blocks obtained af-  
... load/store architecture, with standard integer, floating point (including **fused multiply-add**  
type of operations) and memory operations. ...

[lap.epfl.ch/.../JainSep04\\_AutomaticallyCustomisingVliwArchitecturesWithCoarseGrainedApplication...](http://lap.epfl.ch/.../JainSep04_AutomaticallyCustomisingVliwArchitecturesWithCoarseGrainedApplication...)

[Pdf - A New Speculation Technique to Optimize Floating-Point ...](#)

by M Takeuchi - 2003 - Related articles

**fused multiply-add** (FMA) on some processors. This is one of .... straight-line case is  
usually located at the bottom of the **compiled** code (Figure 1). .... Our FP speculation  
algorithm runs on a direct acyclic graph (**DAG**) ...

[portal.acm.org/ft\\_gateway.cfm?id=782857&type=pdf](http://portal.acm.org/ft_gateway.cfm?id=782857&type=pdf)

Did you mean to search for: [fused multiply add dag \*\*compiler\*\*](#)

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#)

[Next](#)

fused multiply add dag compile

Search

[Search within results](#)

[Search Help](#)

[Give us feedback](#)

[Google Home](#)

[Advertising Programs](#)

[Business Solutions](#)

[Privacy](#)

[About Google](#)

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▼](#)
[Web History](#) | [Search settings](#) | [Sign in](#)


fused multiply add dag table

Search

Instant is off ▼

About 5,000 results (0.64 seconds)

[Advanced search](#)

Everything

[More](#)

Search near...

Enter location

Set

[Show search tools](#)

[\[PDF\] View or Download - AUTOMATIC GENERATION OF IMPLEMENTATIONS FOR DSP ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - Cited by 10 - Related articles

Intel Itanium and Itanium 2, feature **fused multiply-add** (FMA) in- .... acyclic graph or **DAG** describing the data flow of the computation. .... **Table 1**. Best arithmetic cost found by SPIRAL for standard DAGs ...

[citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.58...](http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.58...)

[\[PDF\] Mechanical Derivation of Fused Multiply - SEVERAL modern processor ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by Y Voronenko - 2007 - Related articles

55, NO. 9, SEPTEMBER 2007. **TABLE II. DAG TRANSFORMATION RULES. B.**

Analysis ..... tions for DSP transforms on **fused multiply-add** architectures," in Proc. ... [spiral.ece.cmu.edu:8080/pub-spiral/pubfile/jfma\\_51.pdf](http://spiral.ece.cmu.edu:8080/pub-spiral/pubfile/jfma_51.pdf)

[Automatically Tuned FFTs for BlueGene/L's Double FPU](#)

by F Franchetti - 2005 - Related articles

nodes of the vector **DAG**. **Table 1** shows an example of **fusing** two additions .... cial **fused multiply add** instructions (FMA) provided by BlueGene/L's double ...

[www.springerlink.com/index/mf5lk0k89q8xlqfw.pdf](http://www.springerlink.com/index/mf5lk0k89q8xlqfw.pdf)

[\(WO/2007/114722\) AN OPTIMAL FLOATING-POINT EXPRESSION TRANSLATION ...](#)

by KS SEREBRYANY - 2007

Oct 11, 2007 ... In one embodiment, a **table** of patterns is generated. Each pattern in the **table** includes an FMA (**fused multiply-add**) **DAG** (Directed Acyclic ...

[www.wipo.int/pctdb/en/wo.jsp?WO=2007114722](http://www.wipo.int/pctdb/en/wo.jsp?WO=2007114722) - Cached

[FNMA patent updates](#)

In one embodiment, a **table** of patterns is generated. Each pattern in the **table** includes an FMA (**fused multiply-add**) **DAG** (Directed Acyclic Graph), ...

[tgs.freshpatents.com/Fnma-bx1.php](http://tgs.freshpatents.com/Fnma-bx1.php) - Cached

[\[PDF\] Automatically Optimized FFT Codes for the BlueGene/L Supercomputer](#)

File Format: PDF/Adobe Acrobat - Quick View

ports the extraction of SIMD **fused multiply-add** instructions. ... numerical scalar **DAG** as vectorization input, where the inherent parallelism is ... **Table 1**. Two-way Vectorization. Two scalar **add** instructions are transformed into ...

[www.lseclab.org/people/pw/papers/vecpar04](http://www.lseclab.org/people/pw/papers/vecpar04)

[\[PDF\] \[ensl-00269219. v1\] Integer and Floating-Point Constant ...](#)

File Format: PDF/Adobe Acrobat - Quick View

by N Bricebarre - 2008 - Cited by 7 - Related articles

gorithm which consists of two dependent **fused-multiply-** and **add** .... in **Table 1** show. On the one side, it doesn't try to balance the **DAG** to minimise the latency. ... **add** the overhead of the registers, essentially for the lower ...

[hal.inria.fr/docs/00/26/92/19/PDF/constmult.pdf](http://hal.inria.fr/docs/00/26/92/19/PDF/constmult.pdf)

[Multiple Constant Multiplication By Time-Multiplexed Mapping of ...](#)

by P Tummelshammer - 2004 - Cited by 5 - Related articles

the **table** to feed one input of a full multiplier (shown later in Fig- ure 2(a)). .... additions (a more precise name would be **addition/shift** chain; we .... **fusing** the optimal **DAGs** for **multiplying** by 45 (a) and 19 (b). Each ...

[doi.ieeecomputersociety.org/10.1109/SAC.2004.1322596](http://doi.ieeecomputersociety.org/10.1109/SAC.2004.1322596)

Integer and Floating-Point Constant Multipliers for FPGAs

by N Brisebarre - 2008 - Cited by 7 - Related articles

sists of two dependent **fused-multiply-and-add** operations. .... **table**, obtained using the binary **DAG** approach: mult. by  $\pi/2$ , wFX = 52 ...

ieeexplore.ieee.org/iel5/4569858/.../04580184.pdf?... - Similar

[PDF] ECM on Graphics Cards

File Format: PDF/Adobe Acrobat - Quick View

by DJ Bernstein - Cited by 18 - Related articles

Jan 27, 2009 ... not have the **fused multiply-and-add** instruction; another reason is .... in the **table**. We are unable to make parallel arithmetic run on G80 .... Kazumaro Aoki, Jens

Franke, Thorsten Kleinjung, Arjen K. Lenstra, **Dag** Arne ...

eeecm.cr.yp.to/gpuecm-20090127.pdf - Similar

1 2 3 4 5 6 7 8 9 10

Next

fused multiply add dag table

Search

Search within results

Search Help

Give us feedback

Google Home

Advertising Programs

Business Solutions

Privacy

About Google